## **ABSTRACT**

A priority-based arbiter for arbitrating access to a shared resource by at least two competing devices. The priority-based arbiter intercepts access request signals generated by at least one of the competing devices and generates a respective modified bus request signal. The respective modified bus request signal may be delayed for a predetermined period of time associated with the state (e.g., idle, low priority context, and high priority context) of at least one of the competing devices.